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| (5 AND 6).USPT. | 3 |
| (L6 AND L5).USPT. | 3 |

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result set

*DB=USPT; PLUR=YES; OP=ADJ*L7 l6 and l5

3

L7L6 graphic controller or AGP

3360

L6L5 L4

23

L5*DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*L4 L3 same l2

28

L4L3 (load or loading or store or storing) same (address or addresses) same
l1

1191

L3L2 (controller or (control unit)) adj4 l1

122

L2L1 translation lookaside buffer or TLB

3625

L1

END OF SEARCH HISTORY

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| Term | Documents |
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| SHARE.DWPI,TDBD,EPAB,JPAB,USPT. | 104600 |
| SHARES.DWPI,TDBD,EPAB,JPAB,USPT. | 25345 |
| SHARING.DWPI,TDBD,EPAB,JPAB,USPT. | 63678 |
| SHARINGS.DWPI,TDBD,EPAB,JPAB,USPT. | 23 |
| (31 SAME (SHARING OR SHARE)).USPT,JPAB,EPAB,DWPI,TDBD. | 0 |
| ((SHARE OR SHARING) SAME L31).USPT,JPAB,EPAB,DWPI,TDBD. | 0 |

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| <i>DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | |
| <u>L32</u> (share or sharing) same l31 | 0 | <u>L32</u> |
| <u>L31</u> L30 | 298 | <u>L31</u> |
| <i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | |
| <u>L30</u> L29 or GART | 328 | <u>L30</u> |
| <u>L29</u> graphic adj address adj (redirection or remapping or remap or redirecting or redirect) adj table | 72 | <u>L29</u> |
| <u>L28</u> GART same TLB | 12 | <u>L28</u> |
| <u>L27</u> bridge same GART same TLB | 2 | <u>L27</u> |
| <u>L26</u> address adj2 (translation or translating or translator) | 7704 | <u>L26</u> |
| <u>L25</u> bridge same GART | 45 | <u>L25</u> |
| <u>L24</u> bridge same (bus adj controller) same GART | 0 | <u>L24</u> |
| <u>L23</u> '350' and l22 | 1 | <u>L23</u> |
| <u>L22</u> 6370624.pn. | 2 | <u>L22</u> |
| <u>L21</u> graphic adj L4 | 8 | <u>L21</u> |
| <u>L20</u> l19 and l2 and l1 and (l4 or l5) | 1 | <u>L20</u> |
| <u>L19</u> graphics adj address adj (remapping or remapping or remap) adj table | 72 | <u>L19</u> |
| <u>L18</u> (memory controller or memory management unit) | 29413 | <u>L18</u> |
| <u>L17</u> L16 and l15 | 0 | <u>L17</u> |
| <u>L16</u> (l4 or l5) same l1 | 107 | <u>L16</u> |
| <u>L15</u> (l4 or l5) same l2 | 64 | <u>L15</u> |
| <u>L14</u> (l4 or l5) same l2 same l1 | 0 | <u>L14</u> |
| <u>L13</u> l1 and l2 and (l4 or l5) | 65 | <u>L13</u> |
| <u>L12</u> l11 and l1 and l2 and l3 | 2 | <u>L12</u> |
| <u>L11</u> (l4 or l5).ti,ab. | 3179 | <u>L11</u> |
| <u>L10</u> l1 and l2 and l3 and (l4 or l5) | 53 | <u>L10</u> |
| <u>L9</u> l1L8 | 0 | <u>L9</u> |
| <u>L8</u> L7 and (l4 or l5) | 1 | <u>L8</u> |
| <u>L7</u> l1 same l2 same l3 | 42 | <u>L7</u> |
| <u>L6</u> l1 same l2 same l3 same (l4 or l5) | 0 | <u>L6</u> |
| <u>L5</u> address adj2 (translator or translation or translating) | 7704 | <u>L5</u> |
| <u>L4</u> translation lookaside buffer or TLB | 3615 | <u>L4</u> |
| <u>L3</u> memory controller | 26982 | <u>L3</u> |
| <u>L2</u> graphics adj (processor or controller) | 7671 | <u>L2</u> |
| <u>L1</u> bus controller | 8754 | <u>L1</u> |

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| (14 SAME 13 SAME 1).USPT. | 14 |
| (L13 SAME L14 SAME L1).USPT. | 14 |

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L15

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DB=USPT; PLUR=YES; OP=ADJ

| | | | |
|------------|--|------|------------|
| <u>L15</u> | l13 same l14 same l1 | 14 | <u>L15</u> |
| <u>L14</u> | (physical or real) adj2 address adj2 register | 469 | <u>L14</u> |
| <u>L13</u> | (virtual or linear) adj2 address adj2 register | 399 | <u>L13</u> |
| <u>L12</u> | L11 same l10 same l1 | 15 | <u>L12</u> |
| <u>L11</u> | register adj4 ((linear or virtual) adj2 address) | 665 | <u>L11</u> |
| <u>L10</u> | register adj4 ((physical or real) adj2 address) | 780 | <u>L10</u> |
| <u>L9</u> | L7 same l6 same l1 | 300 | <u>L9</u> |
| <u>L8</u> | L7 and l6 and l1 | 538 | <u>L8</u> |
| <u>L7</u> | register same ((physical or real) adj2 address) | 2860 | <u>L7</u> |
| <u>L6</u> | register same ((linear or virtual) adj2 address) | 2151 | <u>L6</u> |
| <u>L5</u> | L4 and l2 | 73 | <u>L5</u> |
| <u>L4</u> | l1.ti,ab. | 258 | <u>L4</u> |
| <u>L3</u> | L2 | 217 | <u>L3</u> |

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

| | | | |
|-----------|--|------|-----------|
| <u>L2</u> | (compare or comparing or comparison) same register same l1 | 303 | <u>L2</u> |
| <u>L1</u> | translation lookaside buffer or TLB | 3605 | <u>L1</u> |

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| (9 AND 20).USPT,JPAB,EPAB,DWPI,TDBD. | 94 |
| (L9 AND L20).USPT,JPAB,EPAB,DWPI,TDBD. | 94 |

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| <u>Set Name</u> | <u>Query</u> | <u>Hit Count</u> | <u>Set Name</u> |
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| <i>DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <u>L22</u> | l9 and l20 | 94 | <u>L22</u> |
| <u>L21</u> | L20 and l11 | 0 | <u>L21</u> |
| <u>L20</u> | translation lookaside buffer or TLB | 3300 | <u>L20</u> |
| <u>L19</u> | 4638426.pn. | 2 | <u>L19</u> |
| <u>L18</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphic) same l9 | 160 | <u>L18</u> |
| <u>L17</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphics) same l9 | 160 | <u>L17</u> |
| <u>L16</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output)) same l9 | 142 | <u>L16</u> |
| <u>L15</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output) or controller) same l9 | 272 | <u>L15</u> |
| <u>L14</u> | direct memory access or DMA or peripheral or (I adj O) or (input adj output or controller) same l9 | 897068 | <u>L14</u> |
| <u>L13</u> | direct memory access or DMA or peripheral or (I adj O) or (input adj output) and l9 | 897309 | <u>L13</u> |
| <u>L12</u> | graphics and l11 | 2 | <u>L12</u> |
| <u>L11</u> | l9.ti. | 80 | <u>L11</u> |
| <u>L10</u> | L9 and l8 | 6 | <u>L10</u> |
| <u>L9</u> | (extend or extending or expand or expanding) adj4 (address or addressing) | 2579 | <u>L9</u> |
| <u>L8</u> | (table or map) same (translate or translating or convert or converting or translating or mapping or conversion or translation) same ('32' adj bit) same (('36' or '40' or '64') adj bit) same address | 83 | <u>L8</u> |
| <u>L7</u> | L6 | 473 | <u>L7</u> |
| <i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <u>L6</u> | (translate or translating or translation or map or mapping) same address same ((extend or extended or expand or expanded or expanding or extending) adj4 (address or address range)) | 509 | <u>L6</u> |
| <u>L5</u> | (translate or translating or translation or map or mapping) same address same (extend or extended or expand or expanded or expanding or extending) same (address or address range) | 2479 | <u>L5</u> |
| <u>L4</u> | l1 and l2 | 0 | <u>L4</u> |
| <u>L3</u> | L2 same l1 | 0 | <u>L3</u> |
| <u>L2</u> | graphics address redirection table or GART | 320 | <u>L2</u> |
| <u>L1</u> | (extend or extending or expand or expanding) adj4 (address or addressing) adj4 range | 176 | <u>L1</u> |

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| PHYSICAL.USPT. | 486072 |
| PHYSICALS.USPT. | 326 |
| PAGE.USPT. | 206078 |
| PAGES.USPT. | 172016 |
| ADDRESS.USPT. | 216001 |
| ADDRESSES.USPT. | 127549 |
| LARGER.USPT. | 900475 |
| LARGERS.USPT. | 13 |
| BIGGER.USPT. | 14111 |
| BIGGERS.USPT. | 178 |
| LONGER.USPT. | 666382 |
| ((PHYSICAL ADJ2 (PAGE OR ADDRESS)) ADJ4 (LARGER OR BIGGER OR LONGER) SAME ((VIRTUAL OR LOGICAL OR LINEAR) ADJ2 (PAGE OR ADDRESS))).USPT. | 25 |

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result set

DB=USPT; PLUR=YES; OP=ADJ

| | | | |
|------------|---|------|------------|
| <u>L25</u> | (physical adj2 (page or address)) adj4 (larger or bigger or longer) same ((virtual or logical or linear) adj2 (page or address)) | 25 | <u>L25</u> |
| <u>L24</u> | l1 same l19 | 3 | <u>L24</u> |
| <u>L23</u> | (remap or remapping or mapping or map or translate or convert or translating) same (peripheral or (I adj O) or (input adj output) or DMA or graphic) same address same ((high or upper or ('above' adj2 4GB)) adj4 (memory or address or address space or address range)) | 138 | <u>L23</u> |
| <u>L22</u> | L21.ti,ab. | 32 | <u>L22</u> |
| <u>L21</u> | (remap or remapping or mapping or map or translate or convert or translating) same (peripheral or (I adj O) or (input adj output) or DMA or graphic) same address same (high or upper or ('above' adj2 4GB)) same (memory or address or address space or address range) | 618 | <u>L21</u> |
| <u>L20</u> | (extend or extending or expand or expanding) adj4 (address or addressing) adj4 (capability) | 64 | <u>L20</u> |
| <u>L19</u> | (extend or extending or expand or expanding) same (address or addressing) same (capability) | 678 | <u>L19</u> |
| <u>L18</u> | l10 and l1 | 32 | <u>L18</u> |
| <u>L17</u> | (concatenate or combine or add) same address same ((expanded or expand or extend or extended) adj address) | 38 | <u>L17</u> |
| <u>L16</u> | (convert or converting or translate or translating or map or mapping) same address same ((larger or expanded or extended) adj2 address) | 211 | <u>L16</u> |
| <u>L15</u> | l13 same l14 | 160 | <u>L15</u> |
| <u>L14</u> | ((physical or real) adj2 address) same (larger or larger or bigger) same ((linear or logical or virtual) adj2 address) | 365 | <u>L14</u> |
| <u>L13</u> | (convert or converting or translate or translating or map or mapping) same ((logical or virtual or linear) adj2 address) same ((physical or real) adj2 address) | 3388 | <u>L13</u> |
| <u>L12</u> | L11 and l2 | 43 | <u>L12</u> |
| <u>L11</u> | L10 | 432 | <u>L11</u> |

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

| | | | |
|------------|---|------|------------|
| <u>L10</u> | ((711/220)!.CCLS.) | 453 | <u>L10</u> |
| <u>L9</u> | 4638426[uref] | 63 | <u>L9</u> |
| <u>L8</u> | 4638426.pn. | 2 | <u>L8</u> |
| <u>L7</u> | (convert or converting or translate or translating or map or mapping) same (first address) same (larger or large) same (second address) | 8 | <u>L7</u> |
| <u>L6</u> | (table or map) same (convert or converting or translate or translating or map or mapping) same l2 | 338 | <u>L6</u> |
| <u>L5</u> | L4 and l1 | 4 | <u>L5</u> |
| <u>L4</u> | l2.ti. | 260 | <u>L4</u> |
| <u>L3</u> | l1 same l2 | 44 | <u>L3</u> |
| <u>L2</u> | (extend or extended or extending or expand or expanding or expansion) adj4 (address or addressing) | 5027 | <u>L2</u> |

L1 (translation lookaside buffer or TLB)

3605 L1

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| DIRECT.DWPI,TDBD,EPAB,JPAB,USPT. | 948847 |
| DIRECTS.DWPI,TDBD,EPAB,JPAB,USPT. | 130436 |
| MEMORY.DWPI,TDBD,EPAB,JPAB,USPT. | 1159560 |
| MEMORIES.DWPI,TDBD,EPAB,JPAB,USPT. | 133644 |
| MEMORYS.DWPI,TDBD,EPAB,JPAB,USPT. | 152 |
| ACCESS.DWPI,TDBD,EPAB,JPAB,USPT. | 718382 |
| ACCESSES.DWPI,TDBD,EPAB,JPAB,USPT. | 65530 |
| DMA.DWPI,TDBD,EPAB,JPAB,USPT. | 24572 |
| DMAS.DWPI,TDBD,EPAB,JPAB,USPT. | 435 |
| PERIPHERAL.DWPI,TDBD,EPAB,JPAB,USPT. | 859267 |
| PERIPHERALS.DWPI,TDBD,EPAB,JPAB,USPT. | 19409 |
| ((DIRECT MEMORY ACCESS OR DMA OR PERIPHERAL OR (I ADJ O) OR (INPUT ADJ OUTPUT)OR GRAPHIC) SAME L9).USPT,JPAB,EPAB,DWPI,TDBD. | 160 |

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| side by side | | | result set |
| <i>DB=USPT,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <u>L18</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphic) same l9 | 160 | <u>L18</u> |
| <u>L17</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output)or graphics) same l9 | 160 | <u>L17</u> |
| <u>L16</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output)) same l9 | 142 | <u>L16</u> |
| <u>L15</u> | (direct memory access or DMA or peripheral or (I adj O) or (input adj output) or controller) same l9 | 272 | <u>L15</u> |
| <u>L14</u> | direct memory access or DMA or peripheral or (I adj O) or (input adj output or controller) same l9 | 897068 | <u>L14</u> |
| <u>L13</u> | direct memory access or DMA or peripheral or (I adj O) or (input adj output) and l9 | 897309 | <u>L13</u> |
| <u>L12</u> | graphics and l1 l | 2 | <u>L12</u> |
| <u>L11</u> | l9.ti. | 80 | <u>L11</u> |
| <u>L10</u> | L9 and l8 | 6 | <u>L10</u> |
| <u>L9</u> | (extend or extending or expand or expanding) adj4 (address or addressing) | 2579 | <u>L9</u> |
| <u>L8</u> | (table or map) same (translate or translating or convert or converting or translating or mapping or conversion or translation) same ('32' adj bit) same (('36' or '40' or '64') adj bit) same address | 83 | <u>L8</u> |
| <u>L7</u> | L6 | 473 | <u>L7</u> |
| <i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i> | | | |
| <u>L6</u> | (translate or translating or translation or map or mapping) same address same ((extend or extended or expand or expanded or expanding or extending) adj4 (address or address range)) | 509 | <u>L6</u> |
| <u>L5</u> | (translate or translating or translation or map or mapping) same address same (extend or extended or expand or expanded or expanding or extending) same (address or address range) | 2479 | <u>L5</u> |
| <u>L4</u> | l1 and l2 | 0 | <u>L4</u> |
| <u>L3</u> | L2 same l1 | 0 | <u>L3</u> |
| <u>L2</u> | graphics address redirection table or GART | 320 | <u>L2</u> |
| <u>L1</u> | (extend or extending or expand or expanding) adj4 (address or addressing) adj4 range | 176 | <u>L1</u> |

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